

ABSTRACT OF THE DISCLOSURE

An image processing apparatus capable of preventing main scanning magnification discrepancies among a plurality of image data at low cost and with high precision. The apparatus includes a memory for storing control information specified by a CPU, an oscillator for generating a clock having a basic period equivalent to that of a unit pixel or less; variable frequency generators for adjusting a frequency of the clock to a predetermined level independently of each other on the basis of the control information; an image input connection for receiving predetermined data from an external device; image processors for converting parallel image data to serial image data on the basis of a frequency of clock outputted from associated one of the variable frequency generators; and an image output connection for transferring the serial image data to an external device.

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